

FEB 08 2005

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To:	Examiner: Quang D. Vu	From:	Leslie S. Szivos, Ph.D.
Fax:	703 872-9306	Pages:	16 Including cover sheet
Phone:		Date:	2/8/2005
Re:	Appl. No. 10/763,308 Filed: January 23, 2004 Docket No. FIS920020200 (16106A)	CC:	

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Attached hereto is:

1. Response Under 37 C.F.R. §1.116
2. Amendment Transmittal (in duplicate)
3. Certificate of Facsimile

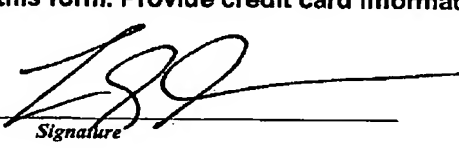
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[illegible]

AMENDMENT TRANSMITTAL LETTER (Large Entity)					Docket No. FIS920020200US2 (16106A)	
Applicant(s): Shreesh Narasimha, et al.						
Application No. 10/763,308	Filing Date January 23, 2004	Examiner Quang D. Vu	Customer No. 23389	Group Art Unit 2811	Confirmation No. 6529	
Invention: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE						
COMMISSIONER FOR PATENTS:						
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.						
CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE	
TOTAL CLAIMS	14 -	20 =	0	x \$50.00	\$0.00	
INDEP. CLAIMS	1 -	3 =	0	x \$200.00	\$0.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00	
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0458/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17. <input type="checkbox"/> Payment by credit card. Form PTO-2038. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.						
 _____ Signature			Dated: February 8, 2005			
Leslie S. Szivos Registration No. 39,394			<div style="border: 1px solid black; padding: 5px;"><input checked="" type="checkbox"/> I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ (Date) _____ Signature of Person Mailing Correspondence _____ Typed or Printed Name of Person Mailing Correspondence</div>			
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P11LARGE/REV09

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Applicant(s): Shreesh Narasimha, et al.						
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Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00	
<input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0458/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17. <input type="checkbox"/> Payment by credit card. Form PTO-2038. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.						
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FEB 08 2005 Response Under 37 C.F.R. § 1.116
Expedited Procedure
Examining Group: 2811**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Shreesh Narasimha, et al. **Examiner:** Quang D. Vu
Serial No.: 10/763,308 **Art Unit:** 2811
Filed: January 23, 2004 **Docket:** FIS920020200US2 (16106A)
Dated: February 8, 2005
For: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE

Confirmation No.: 6529

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. § 1.116

Sir:

In response to the Office Action dated December 14, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: February 8, 2005


Leslie S. Szivos, Ph.D.